

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: To be assigned
Filing Date: Herewith
Applicants: Inao Toyoda et al.
Group Art Unit: To be assigned
Examiner: To be assigned
Title: Photo Sensing Integrated Circuit Device And Related Circuit Adjustment
Attorney Docket: 4041P-000005/DVA

Director of the United States Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

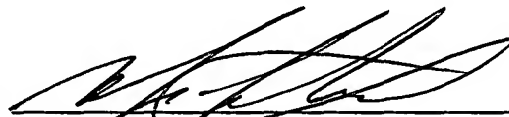
In accordance with the statutory provisions set forth in 37 CFR §1.97 and 1.98, the Examiner is hereby respectfully advised of the references listed on the attached PTO-1449 Form(s). All of these references were made of record during prosecution of the parent application from which this divisional claims priority.

The listed references are cited only in the interest of candor and without any admission that they constitute statutory prior art, or contain matter which anticipates the invention or which would render the same obvious, either singly or in combination, to a person of ordinary skill in the art.

Respectfully submitted,

Dated: July 3, 2003

By


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Reg. No. 34,007

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FORM HDP-1449 (Based on Form PTO-1449) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) Sheet 1 of 2	ATTORNEY DOCKET NO.	SERIAL No.
	4041P-000005/DVA	To be assigned
	APPLICANT	
	Inao Toyoda et al.	
	FILING DATE	GROUP
	Herewith	To be assigned

U.S. PATENT DOCUMENTS						
Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.		5,266,529	11/1993	Lau et al.		
2.		5,602,384	02/1997	Nunogaki et al.		
3.		5,736,756	04/07/1998	Wakayama et al.	257	223
4.		5,293,051	03/08/1994	Mariyama et al.	257	129
5.		5,239,193	08/24/1993	Benton et al.	257	292
6.		5,223,728	06/29/1993	Gempe	257	290
7.		5,117,118	05/26/1992	Fukuyama	250	551
8.		5,105,090	04/14/1992	Miyajima et al.	257	551
9.		5,101,253	03/31/1992	Mizutani et al.	357	30
10.		4,841,138	06/20/1989	Muro	250	209
11.		5,426,526	06/1995	Yamamoto et al.	359	82
12.		3,952,323	04/1976	Tanimura et al.	357	15
13.		5,811,867	09/1998	Yamamoto et al.	257	458
14.		5,598,022	01/1997	Kyomasu	257	458

FOREIGN PATENT DOCUMENTS						
Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes No
1.		60-133747	07/1985	Japan		Abstract
2.		61-154373	07/1986	Japan		Abstract
3.		62-2653	01/1987	Japan		Abstract
4.		62-202556	09/1987	Japan		Abstract

Examiner:	Date Considered:
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EXAMINER: Please initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM HDP-1449 (Based on Form PTO-1449)

**PATENT AND TRADEMARK OFFICE
INFORMATION DISCLOSURE CITATION**

(Use several sheets if necessary)

Sheet 2 of 2

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FOREIGN PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation	
						Yes	No
5.		63-116458	05/1988	Japan		Abstract	
6.		63-274174	11/1988	Japan		Abstract	
7.		64-54758	03/1989	Japan		Abstract	
8.		3-60073	03/1991	Japan		Abstract	
9.		6-104476	04/1994	Japan		Abstract	
10.		6-326282	11/1994	Japan		Abstract	

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Ref. Desig.	Examiner's Initials	
1.		"DIGITAL CIRCUIT" by Hiroshi INOSE et al.; published April 28, 1980 (w/English Abstract)

Examiner:

Date Considered:

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